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22850 7590 03/12/2004

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/717,556	11/21/2003	Koji Miyamoto	245367US2SCONT	8472

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING MULTILEVEL INTERCONNECTION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/14/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
WOJCIECHOWICZ, EDWARD JOSEPH	2815	438-597000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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1. OBLON, SPIVAK,  
2. McCLELLAND, MAIER  
3. & NEUSTADT, P.C.

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(A) NAME OF ASSIGNEE

KABUSHIKI KAISHA TOSHIBA

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent);  individual  corporation or other private group entity  government

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*Joseph A. Scafetta Jr. Reg. No. 26,803 June 14, 2004*

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06/16/2004 FFANAIAS 00000075 10717556

01 FC:1501	1330.00 OP
02 FC:1504	300.00 OP
03 FC:8001	30.00 OP